## **EAST Search History**

Ref#	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	33	path same ((hdl) (vhdl) (hardware adj description adj language) (verilog)) same compil\$4 and (717/123 717/13 717/144 717/155 717/154 716/5 716/16 716/18)	US-PGPUB; USPAT; FPRS; EPO; JPO; DEFWENT; IBM_TDB	OR	ON	2009/04/23
L3	14	(((hdl) (vhdl) (hardware adj description adj language) (verilog)) same (register near1 transfer near1 language (rtl)) same (simulat\$4 test\$4) same path and net \$list) and (717/123 717/144 717/156 717/157 716/3 716/5 716/16	US-PGPUB; USPAT; FPPS; EPC; JPC; DEFWENT; IBM_TDB	OR	ON	2009/04/23 16:32
L4	40	path same ((hdl) (vhdl) (hardware ad) description adj language) (verilog) rtl) same compil\$\(^4\) and (integrated adj circuit) and (717/123 717/13 717/144 717/155 717/156 717/137 716/3 716/5 716/16 716/18 )	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/23

L5	14	path same ((hdl) (vhdl) (hardware adj description adj language) (verilog) rtl) same compil\$4 and index and (717/123 717/138 717/143 717/145 717/156 717/157 716/5 716/16 716/18)	US PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/23 16:33
L6	158	(((hdl) (vhdl) (hardware ad) description ad) language) (verilog) rtl) same compil\$4 and index and (717/123 717/138 717/143 717/144 717/155 717/156 717/137 716/3 716/5 716/16	US-PGPUB; USPAT; FPRS; EPO; JPO; DEFWENT; IBM_TDB	OR	ON	2009/04/23 16:33
L7	17	(((hdl) (vhdl) (hardware adj description adj language) (verilog) rtl) same compil\$4 and index.clm. and (717/123 717/138 717/143 717/144 717/155 717/156 716/16 716/18)	US POPUB; USPAT; FPRS; EPO; JPO; EPO; JPO; IBM_TDB	OR	ON	2009/04/23 16:34
L8	1	(((hdl) (vhdl) (hardware adj description adj language) (verilog) rtl) same compil\$4 and index.dm. and (717/123 717/138 717/143 717/144 717/155 717/156 716/16 716/18)	US-PGPUB	OR	ON	2009/04/23 16:34

L9	11	(net\$list) same ((hdl) (vhdl) (hardware adj description adj language) (verilog)) same path and (717/123 717/138 717/143 717/143 717/144717/155 717/156 717/137 716/18)	US-PGPUB	OR	ON	2009/04/23 16:34
L11	3	(generat\$4 construct\$4 creat\$4 build\$4) with ((hardware adj description adj language) (hdl)) same pars\$4 with (description file) same path and (717/123 717/138 717/139 717/136 717/144 717/155 717/156 717/137 716/3 716/16	US-PGPUB; USPAT; USOCR; EPC; UPC; DEFWENT; IBM_TDB	OR .	ON	2009/04/23 16:37
L12	111	(net\$list) same ((hdl) (vhdl) (hardware adj description adj language) (verilog)) same path and (717/123 717/138 717/139 717/13 717/143 717/144 717/155 717/156 717/137 716/3 716/5 716/16 716/18)	US-PGPUB	OR	ON	2009/04/23 16:38
L13	94	((((hdl)) (vhdl)) (hardware adj description adj language) (verilog) asci) same (register near1 transfer near1 language (rtll)) same (simulalts4 test\$4) same (Ic near2 design) and net\$list (file adj path)) and (717/101 717/123 717/138 717/143 717/136 717/155 717/156 717/137 716/3 716/5	US-PGPUB; USPAT; FPFS; EPC, JPC; DEFWENT; IBM_TDB	OR	ON	2009/04/23 16:56

L15	15	(((hdl) (vhdl) (hardware adj description adj language) (verilog) asci) same (register near1 transfer near1 language (rtl)) same (simulat\$4 test\$4) same (to near2 design) and net\$list (file adj path) and (rapid \$make)) and (717/101 717/123 717/138 717/143 717/144 717/155 717/156 717/173 716/5 716/18)	US-PGPUB; USPAT; FPFS; EPC, JPC; DEFWENT; IBM_TDB	OR	ON	2009/04/23 16:56
L16	. 11	(((hdl) (vhdl) (hardware adj description adj language) (verilog) asci) same (register near1 transfer near1 language (rtl)) same (simulat\$4 test\$4) same (10 near2 design) and net\$list (file adj path) and (rapid \$make)) and (717/101 717/123 717/138 717/143 717/144 717/138 717/155 717/156 717/137 716/3 716/5 716/16	US-PGPUB	OR	ON	2009/04/23 16:57
L17	9	(((hdl) (vhdl) (hardware adj description adj language) (verilog) asci) same (register near1 transfer near1 language (rll)) same (simulat84 tets84) same (Ic near2 design) and net\$isti (file adj path)).cim. and (717/101 717/123 717/138 717/143 717/144 717/155 717/156 717/137 716/3 716/5 716/16 716/18)	US-PGPUB; USPAT; FPFS; EPC; JPC; DEFWENT; IBM_TDB	OR	ON	2009/04/23 16:57

L18	40	path same ((hdl) (vhdl) (hardware adj description adj language) (verilog) rtl) same compil\$4 and (integrated adj circuit) and (717/1/23 717/13/717/143 717/145 717/155 717/156 716/16 716/18)	US-PCPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/23 16:57
L19	6	path same ((hdl) (vhdl) (hardware adj description adj language) (verilog) rtl) same compil\$4 same (integrated adj circuit) and (717/123 717/13/ 717/144 717/155 717/156 717/157 716/5 716/16 716/18)	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/23 16:58
L20	15	(((hdl) (vhdl) (hardware adj description adj language) (verilog) asoi) same (register near1 transfer near1 language (rII)) same (simulat\$4 test\$4) same (Ic near2 design) and net\$list (file adj path) and (717/101 717/123 717/138 717/143 717/144 717/155 717/156 717/173 716/3 716/5 716/16 716/18)	US-PGPUB; USPAT; FPFS; EPC; JPC; DEFWENT; IBM_TDB	OR	ON	2009/04/23
L21	15	(((hdl) (vhdl) (hardware adj description adj language) (verilog) asoi) same (register near1 transfer near1 language (rtl)) same (simulat\$4 test\$4) same (Ic near2 design) and net\$list (path) and (rapid\$make)) and (717/101 717/123 717/138 717/143 717/144 717/155	US-PGPUB; USPAT; FPTS; EPO; JPO; DEFEMENT; IBM_TDB	OR	ON	2009/04/23 17:05

		717/156 717/137 716/3 716/5 716/16 716/18 )	***************************************	***************************************	***************************************	
L24	3	(((fldl) (vhdl) (hardware adj description adj language) (verilog) asci) same (Ic near2 design) and net\$list (path) and (rapid \$make)).clm, and (717/101 717/123 717/144 717/135 717/143 717/146 717/137 716/3 716/5 716/16	US-PGPUB; USPAT; FPFS; EPC; JPC; DEFWENT; IBM_TDB	OR	ON	2009/04/23 17:05
S31	7	(pars\$4 analyz44) with (description) same ((hdl) (whdl) (hardware adj description adj language)) same path	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:08
S34	19	(pars\$4 analyz44) with (description) same ((hdl) (vhdl) (hardware adj description adj language)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 13:11
S35	8	(pars\$4 analyz44) with (description) same ((hdl) (vhdl) (hardware adj description adj language)).clm.	US-PGPUB	OR	ON	2009/04/20 13:11
S63	9	(((hdl) (vhdl) (hardware adj description adj language) (verilog) asci) same (register near1 transfer near1 language (rtl)) same (simulat\$4 test\$4) same (Ic near2 design) and net\$ist (file adj path).clm. and (717/101 717/123 717/138 717/143 717/156 717/137 716/3 716/18 )	US-PGPUB; USPAT; FPTS; EPC; JPO; DEFWENT; IBM_TDB	OR	ON	2009/04/20

S64	33	path same ((hdl) (vhdl) (hardware adj description adj language) (verilog)) same compil\$4 and (717/123 717/13/ 717/143 717/156 717/167 716/16 716/16)	US-PCPUB; USPAT; FPRS; EFO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 14:36
S65	51	path same ((fidl) (vhdl) (hardware adj description adj language) (veritog) rtl) same compil\$4 and (717/123 717/13/ 717/143 717/155 717/156 717/167 716/16 716/16 716/16	US-PGPUB; USPAT; FPPS; EFO; JPO; DERWENT; IBM_TDB	OR	ON	2009/04/20 14:36

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